PIN	LABEL	SIGNAL	DESCRIPTION
1	A1	+3V3D	+3V3 1A Power supply
2	A2	25MHz	25MHz system clock
3	A3	+3V3VBT	+3V for RTC
4	A4	ERXD0	Multiplexed serial receive channel 0
5	A5	#AWE	Asynchronous Memory Write Enable
6	A6	#ARE	Asynchronous Memory Read Enable
7	A7	#AMS1	Asynchronous Memory Bank Selects
8	A8	PH0	PH0/DR1PRI/SPI1SS/RSI_DATA4 - *GPIO/SPORT1 Primary Rx Data/SPI1 Device Select/RSI Data 4*
9	A9	PH1	PH1/RFS1/SPI1MISO/RSI_DATA5 - *GPIO/SPORT1 Rx Frame Sync/SPI1 Master In Slave Out/RSI Data 5*
10	A10	PH2	PH2/RSCLK1/SPI1SCK/RSI DATA6 - *GPIO/SPORT1 Rx Clock/SPI1 Clock/RSI Data 6*
11	A11	PH3	PH3/DT1PRI/SPI1MOSI/RSI DATA7 - *GPIO/SPORT1 Primary Tx Data/SPI1 Master Out Slave In/RSI Data 7*
12	A12	PH4	PH4/TFS1/#AOE/#SPI0SEL3/CUD - *GPIO/SPORT1 Tx Frame Sync/Asynchronous Memory Output Enable/SPI0 Slave Select 3/Counter Up Direction*
13	A13	PH5	PH5/TSCLK1/ARDY/PTP_EXT_CLKIN/CDG - *GPIO/SPORT1 Tx Clock/Asynchronous Memory Hardware Ready Control/External Clock for PTP TSYNC/Counter Down Gate*
14	A14	PH6	PH6/DT1SEC/UART1TX/#SPI1SEL1/CZM - *GPIO/SPORT1 Secondary Tx Data/UART1 Transmit/SPI1 Slave Select 1/Counter Zero Marker*
15	A15	PH7	PH7/DR1SEC/UART1RX/TMR7/TACI2 - *GPIO/SPORT1 Secondary Rx Data/UART1 Receive/Timer 7/Timer2 Alternate Clock Input*
16	A16	PG0	PG0/MIICRS/RMIICRS/HWAIT 3 /#SPI1SEL3 - *GPIO/Ethernet MII or RMII Carrier Sense or RMII Data Valid/HWAIT/SPI1 Slave Select3*
17	A17	PG1	PG1/ERxER/DMAR1/PWM CH - *GPIO/Ethernet MII or RMII Receive Error/DMA Req 1/PWM CH Out*
18	A18	PG2	PG2/MIITxCLK/RMIIREF_CLK/DMAR0/PWM CL - *GPIO/Ethernet MII or RMII Reference Clock/DMA Req 0/PWM CL Out*
19	A19	PG3	PG3/DR0PRI/RSI_DATA0/#SPI0SEL5/TACLK3 - *GPIO/SPORT0 Primary Rx Data/RSI Data 0/SPI0 Slave Select 5/Timer3 Alternate CLK*
20	A20	PG4	PG4/RSCLK0/RSI_DATA1/TMR5/TACI5 - *GPIO/SPORT0 Rx Clock/RSI Data 1/Timer 5/Timer5 Alternate Capture Input*
21	A21	PG5	PG5/RFS0/RSI_DATA2/PPICLK/TMRCLK - *GPIO/SPORT0 Rx Frame Sync/RSI Data 2/PPI Clock/External Timer Reference*
22	A22	PG6	PG6/TFS0/RSI_DATA3/TMR0/PPIFS1 - *GPIO/SPORT0 Tx Frame Sync/RSI Data 3/Timer0/PPI Frame Sync1*
23	A23	PG7	PG7/DT0PRI/RSI_CMD/TMR1/PPIFS2 - *GPIO/SPORT0 Tx Primary Data/RSI Command/Timer 1/PPI Frame Sync2*
24	A24	PG8	PG8/TSCLK0/RSI_CLK/TMR6/TACI6 - *GPIO/SPORT0 Tx Clock/RSI Clock/Timer 6/Timer6 Alternate Capture Input*
25	A25	TXD0	PG9/DT0SEC/UART0TX/TMR4 - *GPIO/SPORT0 Secondary Tx Data/UART0 Transmit/Timer 4*
26	A26	RXD0	PG10/DR0SEC/UART0RX/TACI4 - *GPIO/SPORT0 Secondary Rx Data/UART0 Receive/Timer4 Alternate Capture Input*
27	A27	PG11	PG11/#SPI0SS/#AMS2/#SPI1SEL5/TACLK2 - *GPIO/SPI0 Slave Device Select/Asynchronous Memory Bank Select 2/SPI1 Slave Select 5/Timer2 Alternate CLK*
28	A28	PG12	PG12/SPI0SCK/PPICLK/TMRCLK/PTP_PPS - *GPIO/SPI0 Clock/PPI Clock/External Timer Reference/PTP Pulse Per Second Out*
29	A29	PG13	PG13/SPI0MISO/TMR0/PPIFS1/PTP_CLKOUT - *GPIO/SPI0 Master In Slave Out/Timer0/PPI Frame Sync1/PTP Clock Out*
30	A30	PG14	PG14/SPI0MOSI/TMR1/PPIFS2/#PWM TRIP/PTP_AUXIN - *GPIO/SPI0 Master Out Slave In/Timer 1/PPI Frame Sync2/PWM Trip/PTP Auxiliary Snapshot Trigger Input*
31	A31	#BOOT_CS	PG15/#SPI0SEL2/PPIFS3/#AMS3 - *GPIO/SPI0 Slave Select 2/PPI Frame Sync3/Asynchronous Memory Bank Select 3*
32	A32	GND	Power return

	LABEL	SIGNAL	DESCRIPTION
33	B1	VBUS	USB Power supply
34	B2	A01	Address Bus
35	B3	A02	Address Bus
36	B4	A03	Address Bus
37	B5	A04	Address Bus
38	B6	A05	Address Bus
39	B7	A06	Address Bus
40	B8	A07	Address Bus
41	B9	A08	Address Bus
42	B10	A09	Address Bus
43	B11	A10	Address Bus
44	B12	A11	Address Bus
45	B13	A12	Address Bus
46	B14	A13	Address Bus
47	B15	A14	Address Bus
48	B16	A15	Address Bus
49	B17	A16	Address Bus
50	B18	A18	Address Bus
51	B19	A18	Address Bus
52	B20	A19	Address Bus
53	B21	PF0	PF0/ETxD2/PPI D0/#SPI1SEL2/TACLK6 - *GPIO/Ethernet MII Transmit D2/PPI Data 0/SPI1 Slave Select 2/Timer6 Alternate Clock*
54	B22	PF1	PF1/ERxD2/PPI D1/PWM AH/TACLK7 - *GPIO/Ethernet MII Receive D2/PPI Data 1/PWM AH Output/Timer7 Alternate Clock*
55	B23	PF2	PF2/ETxD3/PPI D2/PWM AL - *GPIO/Ethernet Transmit D3/PPI Data 2/PWM AL Output*
56	B24	PF3	PF3/ERxD3/PPI D3/PWM BH/TACLK0 - *GPIO/Ethernet MII Data Receive D3/PPI Data 3/PWM BH Output/Timer0 Alternate Clock*
57	B25	----	
58	B26	PF4	PF4/ERxCLK/PPI D4/PWM BL/TACLK1 - *GPIO/Ethernet MII Receive Clock/PPI Data 4/PWM BL Out/Timer1 Alternate CLK*
59	B27	PF5	PF5/ERxDV/PPI D5/PWM CH/TACI0 - *GPIO/Ethernet MII Receive Data Valid/PPI Data 5/PWM CH Out/Timer0 Alternate Capture Input*
60	B28	PF6	PF6/COL/PPI D6/PWM CL/TACI1 - *GPIO/Ethernet MII Collision/PPI Data 6/PWM CL Out/Timer1 Alternate Capture Input*
61	B29	PF7	PF7/#SPI0SEL1/PPI D7/PWMSYNC - GPIO/SPI0 Slave Select 1/PPI Data 7/PWM Sync
62	B30	PF8	PF8/MDC/PPI D8/#SPI1SEL4 - *GPIO/Ethernet Management Channel Clock/PPI Data 8/SPI1 Slave Select 4*
63	B31	PF9	PF9/MDIO/PPI D9/TMR2 - *GPIO/Ethernet Management Channel Serial Data/PPI Data 9/Timer 2*
64	B32	GND	Power return

	LABEL	SIGNAL	DESCRIPTION
65	C1	+5V	Power supply
66	C2	SCL	TWI Serial Clock
67	C3	SDA	TWI Serial Data
68	C4	PF10	PF10/ETxD0/PPI D10/TMR3 - *GPIO/Ethernet MII or RMII Transmit D0/PPI Data 10/Timer 3*
69	C5	PF11	PF11/ERxD0/PPI D11/PWM AH/TACI3 - *GPIO/Ethernet MII Receive D0/PPI Data 11/PWM AH output /Timer3 Alternate Capture Input*
70	C6	PF12	PF12/ETxD1/PPI D12/PWM AL - *GPIO/Ethernet MII Transmit D1/PPI Data 12/PWM AL Output*
71	C7	PF13	PF13/ERxD1/PPI D13/PWM BH - *GPIO/Ethernet MII or RMII Receive D1/PPI Data 13/PWM BH Output*
72	C8	PF14	PF14/ETxEN/PPI D14/PWM BL - *GPIO/Ethernet MII Transmit Enable/PPI Data 14/PWM BL Out*
73	C9	PF15	PF15/#RMII PHYINT/PPI D15/PWM_SYNCA - *GPIO/Ethernet MII PHY Interrupt/PPI Data 15/Alternate PWM Sync*
74	C10	DB00	Data Bus
75	C11	DB01	Data Bus
76	C12	DB02	Data Bus
77	C13	DB03	Data Bus
78	C14	DB04	Data Bus
79	C15	DB05	Data Bus
80	C16	DB06	Data Bus
81	C17	DB07	Data Bus
82	C18	DB08	Data Bus
83	C19	DB09	Data Bus
84	C20	DB10	Data Bus
85	C21	DB11	Data Bus
86	C22	DB12	Data Bus
87	C23	DB13	Data Bus
88	C24	DB14	Data Bus
89	C25	DB15	Data Bus
90	C26	#UDTR	Data Terminal Ready signal from USB 2 com port
91	C27	#URTS	Request To Send signal from USB 2 com port
92	C28	NMI	Non-maskable Interrupt
93	C29	COMSEL	Enable ERXD0 multiplexer. Active high
94	C30	#RESIN	System RESET input
95	B31	#RESET	System RESET output
96	B32	GND	Power return