Y2 – DSP EXPANSION PORT		
PIN	LABEL	DESCRIPTION
1	PG1–GPIO/DR0PRI/SPI1_SSEL1/WAKEN3	GPIO/SPORT1 Receive Data Primary/PPI Data 9GPIO/SPORT0 Receive Data Primary/SPI1 Slave Select Enable 1/Wake Enable 3
2	PG2–GPIO/RSCLK0/SPI0_SSEL5	GPIO/SPORT0 Receive Serial Clock/SPI0 Slave Select Enable 5
3	PG3–GPIO/RFS0/PPI_FS3	GPIO/SPORT0 Receive Frame Sync/PPI Frame Sync 3
4	PG4–GPIO(HWAIT)/DT0SEC/SPI0_SSEL6	GPIO (HWAIT output for Slave Boot Modes)/SPORT0 Transmit Data Secondary/SPI0 Slave Select Enable 6
5	PG5–GPIO/DT0PRI/SPI1_SSEL6	GPIO/SPORT0 Transmit Data Primary/SPI1 Slave Select Enable 6
6	PG6–GPIO/TSCLK0	GPIO/SPORT0 Transmit Serial Clock
7	PG7–GPIO/TFS0/SPI1_SSEL7	GPIO/SPORT0 Transmit Frame Sync/SPI1 Slave Select Enable 7
8	PG8–GPIO/SPI1_SCK/PPI_D0	GPIO/SPI1 Clock/PPI Data 0
9	PG9–GPIO/SPI1_MOSI/PPI_D1	GPIO/SPI1 Master Out Slave In/PPI Data 1
10	PG10–GPIO/SPI1_MISO/PPI_D2	GPIO/SPI1 Master In Slave Out/PPI Data 2
11	PG11–GPIO/SPI1_SSEL5/PPI_D3	GPIO/SPI1 Slave Select Enable 5/PPI Data 3
12	PG12–GPIO/SPI1_SSEL2/PPI_D4/WAKEN2	GPIO/SPI1 Slave Select Enable 2 Output/PPI Data 4/Wake Enable 2
13	PG13–GPIO/SPI1_SSEL1/SPI1_SS/PPI_D5	GPIO/SPI1 Slave Select Enable 1 Output/PPI Data 5/SPI1 Slave Select Input
14	PG14–GPIO/SPI1_SSEL4/PPI_D6/TACLK1	GPIO/SPI1 Slave Select Enable 4/PPI Data 6/Timer 1 Auxiliary Clock Input
15	PG15–GPIO/SPI1_SSEL6/PPI_D7/TACLK2	GPIO/SPI1 Slave Select Enable 6/PPI Data 7/Timer 2 Auxiliary Clock Input
16	SCL	TWI Serial Clock (This signal is an open-drain output and requires a pull-up resistor.
17	SDA	TWI Serial Data (This signal is an open-drain output and requires a pull-up resistor
18	PF0–GPIO/DR1SEC/PPI_D8/WAKEN1	GPIO/SPORT1 Receive Data Secondary/PPI Data 8/Wake Enable 1
19	PF1–GPIO/DR1PRI/PPI_D9	GPIO/SPORT1 Receive Data Primary/PPI Data 9
20	PF2–GPIO/RSCLK1/PPI_D10	GPIO/SPORT1 Receive Serial Clock/PPI Data 10
21	PF3–GPIO/RFS1/PPI_D11	GPIO/SPORT1 Receive Frame Sync/PPI Data 11
22	PF4–GPIO/DT1SEC/PPI_D12	GPIO/SPORT1 Transmit Data Secondary/PPI Data 12
23	PF5–GPIO/DT1PRI/PPI_D13	GPIO/SPORT1 Transmit Data Primary/PPI Data 13
24	PF6–GPIO/TSCLK1/PPI_D14	GPIO/SPORT1 Transmit Serial Clock/PPI Data 14
25	PF7–GPIO/TFS1/PPI_D15	GPIO/SPORT1 Transmit Frame Sync/PPI Data 15
26	PF9–GPIO/TMR0/PPI_FS1/SPI0_SSEL3	GPIO/Timer 0/PPI Frame Sync 1/SPI0 Slave Select Enable 3
27	PF10–GPIO/TMR1/PPI_FS2	GPIO/Timer 1/PPI Frame Sync 2
28	TxData	Serial transmit
29	RxData	Serial receive
30	PPI_CLK	PPI Clock Input
31	GND	Power return
32	/NMI	Nonmaskable Interrupt

Y1 – POWER SUPPLY		
PIN	LABEL	DESCRIPTION
1	GND	Power return
2	VBAT	1S LiION battery. Is used for power supply and for RTC. It's charged @ 240 mA
3	POWER	5 – 14 V Power supply when available. Automatic switch from battery
4	GND	Power return

Y4 - CONTROLS		
PIN	LABEL	DESCRIPTION
1	+5V	Power supply max 500mA
2	VBUSPWR	USB power supply, used for battery charger
3	EXTSTART	A rising edge turn on the power supply
4	+3V3VBT	RTC power supply
5	/CHARGE	LED charge indicator. Connect to LED cathode
6	+3V3	Power supply max 100mA
7	GND	Power return
8	GND	Power return

JUMPER SETTINGS

J1 – Closed – Read current consumption on ADC7 channel (1V = 1A)
J3 – Closed – Read current consumption on ADC6 channel (1V = 1A)
J4 – Closed – Read current consumption on ADC5 channel (1V = 1A)

J5 – Closed – Read LiION battery voltage on ADC6 channel
J6 – Closed – Read LiION battery voltage on ADC5 channel
J7 – Closed – Read LiION battery voltage on ADC4 channel

J9 – Close force DSP RAM boot